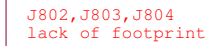
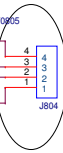


SYNAPTICS CONFIDENTIAL



SA0 SA1 address setting 11
I2C bus already pull high to 3.3V



			Pin	Low	High
CFG1	CFG0		MS	I2C slave	Load EEPROM
Low	High	Stereo	PLL	Enable	disable (MCLK from Ext.)
			DEF	Mute	Un-Mute
			LREX	Unchanged	Exchanged
			LINEIN	SDATA0	SDATA1

Synaptics, Inc.
1251 McKay Dr.
SAN JOSE, CA 95131



BG5-CT_STB_RDK Main Board

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